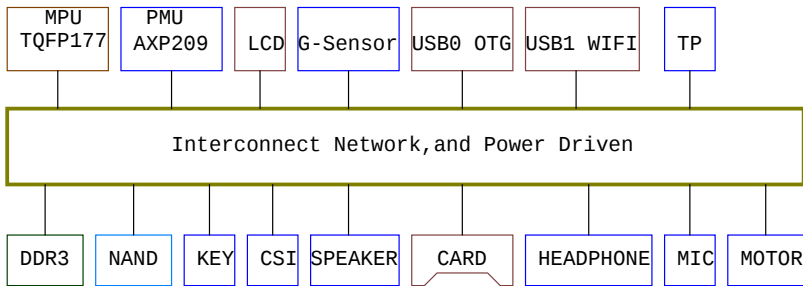


COVER

BLOCK



GPIO ASSIGNMENT

CPU	Define	Function
PB0	TWI0_SCK	TWI0
PB1	TWI0_SDA	
PB2	PWM0	LCD
PB3	GPIO-OUT	TP-WAKE-RST
PB4	NC	
PB10	GPIO-OUT	CSI-STY
PB15	TWI1_SCK	TWI1
PB16	TWI1_SDA	
PB17	TWI2_SCK	TWI2
PB18	TWI2_SDA	
PG0	INPUT	SD0-DET-N
PG1	GPIO-IN	USB0-VBUSDET
PG2	GPIO-IN	USB0-IDDET
PG3	GPIO-OUT	CSI-STY-1
PG4	GPIO-OUT	CSI-RST-1
PG9	GPIO-OUT	MT-EN
PG10	GPIO-OUT	PA-SHDN
PG11	EINT	TP-INT
PG12	GPIO-OUT	USB0-DRV
PMU	Define	Function
GPIO0	GPIO-OUT	LCD-PWR
GPIO1	GPIO-OUT	LCD-BL-EN
GPIO2	GPIO-OUT	CSI-PWR-EN
GPIO3	GPIO-OUT	CSI-RST

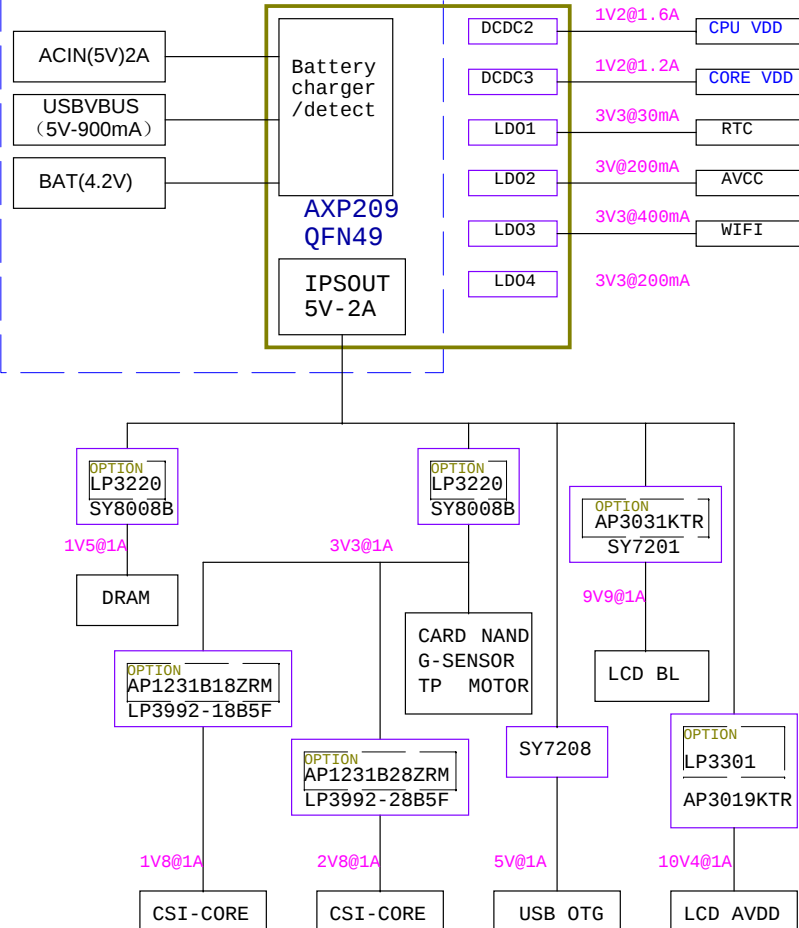
特别提醒:
 1: PG0 / PG1 / PG2这三个PIN脚
 只具有INPUT/中断/专有功能
 2: PMU的GPIO0/1/2/3这四个PIN脚
 只做GPIO-OUTPUT功能
 3: PG10 /PG11 /PG12这三个PIN脚
 的功能不可改变

REVISION HISTORY

Revision	Description	Date	Drawn	Checked	Approved
APP3_PAD_DDR3_V1_22	version 1.22		YT		
APP3_PAD_DDR3_V1_23	version 1.23	2012.03.28	YT		

POWER TREE

LAYOUT: ACIN、BAT、IPSOUT输入或
 输出线，从PMU管脚处就要保证尽量粗。



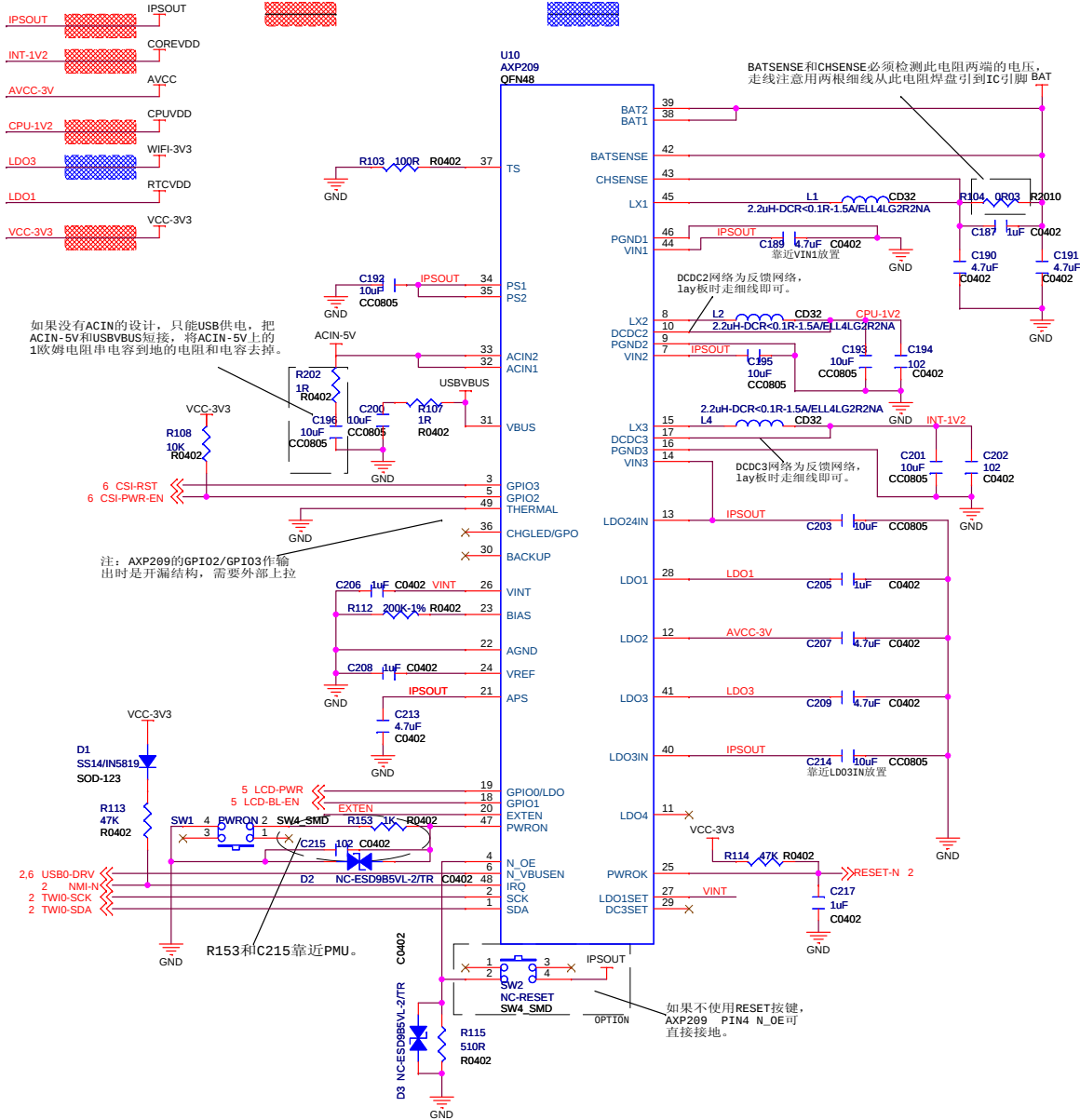
Schematics Index

- 01 COVER
- 02 CPU
- 03 POWER
- 04 MEMORY
- 05 DISPLAY
- 06 MISC
- 07 WIFI

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APP3_PAD_DDR3		
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POWER

POWER LINE:Width>=80mil POWER LINE:Width>=50mil



如果没有ACIN的设计, 只能USB供电, 把ACIN-5V和USBVBUS短接, 将ACIN-5V上的1欧姆电阻中电容到地的电阻和电容去掉。

注: AXP299的GPIO2/GPIO3作输出时是开漏结构, 需要外部上拉

R153和C215靠近PMU。

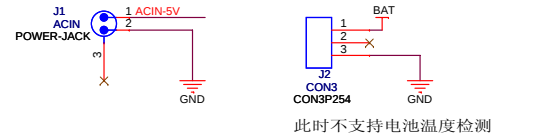
如果不使用RESET按键, AXP209 PIN4 N_OE可直接接地。

注: 2.2uH-DCR<0.1R-1.5A
表示在1.5A的电流下电感感量还满足2.2uH,
DCR<0.1R表示电感直流电阻要小于0.1欧姆。

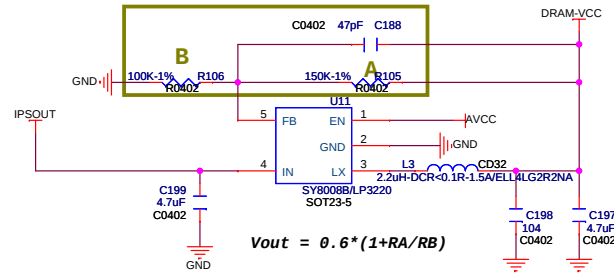
BATSENSE和CHSENSE必须检测此电阻两端的电压, 走线注意用两根细线从此电阻焊盘引到IC引脚 BAT

DCDC2网络为反馈网络, lay板时走细线即可。

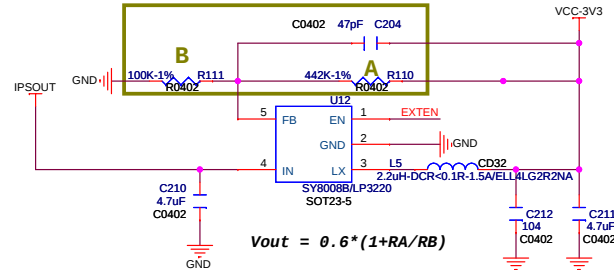
DCDC3网络为反馈网络, lay板时走细线即可。



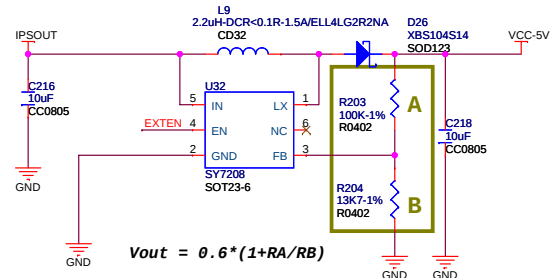
此时不支持电池温度检测



$$V_{out} = 0.6 * (1 + RA/RB)$$



$$V_{out} = 0.6 * (1 + RA/RB)$$



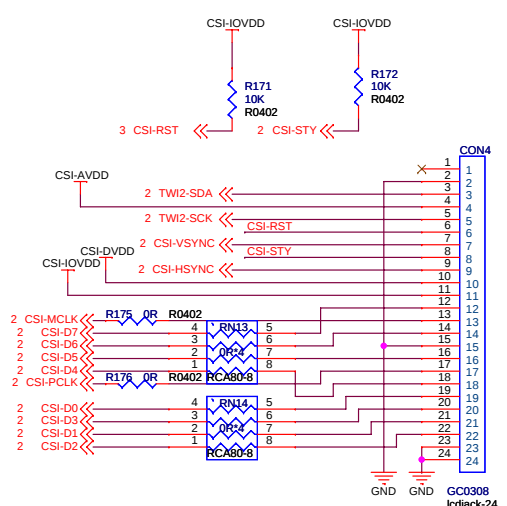
$$V_{out} = 0.6 * (1 + RA/RB)$$

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MISC

CSI

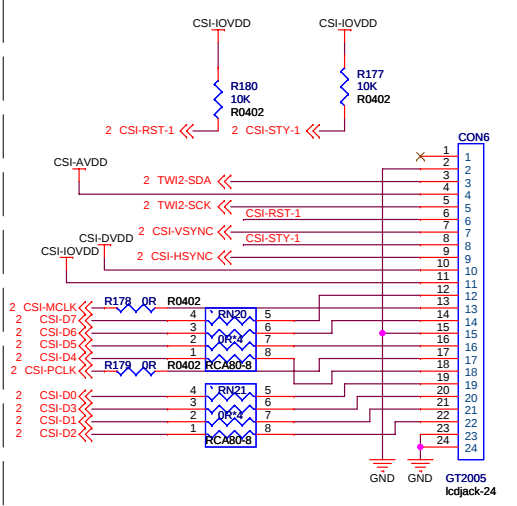
30W-前置低分辨率



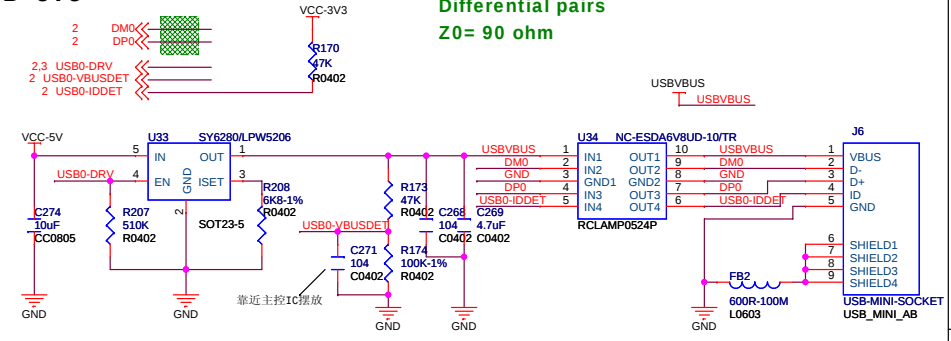
- A. LAYOUT时, 请保证摄像头成像方向与LCD显示一致
- B. 若选用其他模组, 请检查CSI- IOVDD, CSI-AVDD, CSI-DVDD的具体电压值以及负载能力能够满足

OPTION

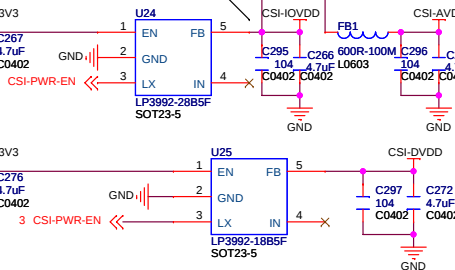
200W-后置高分辨率



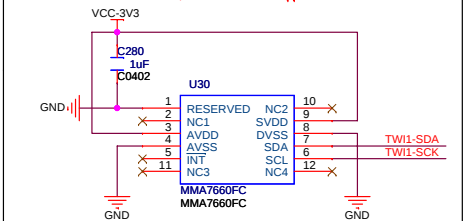
USB-OTG



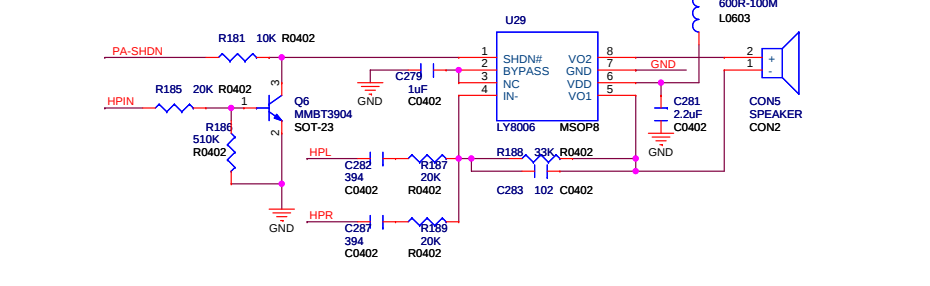
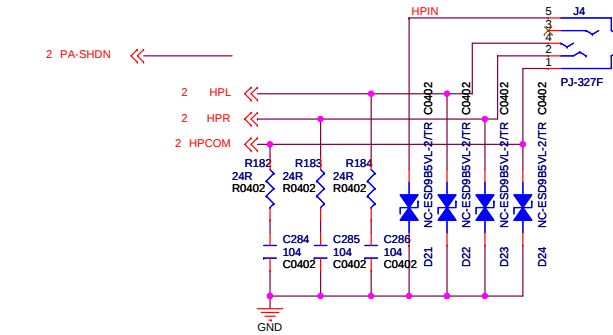
PCB layout时CSI-AVDD电源要与CSI-IOVDD在LDO引脚处分别走线, 防止相互干扰



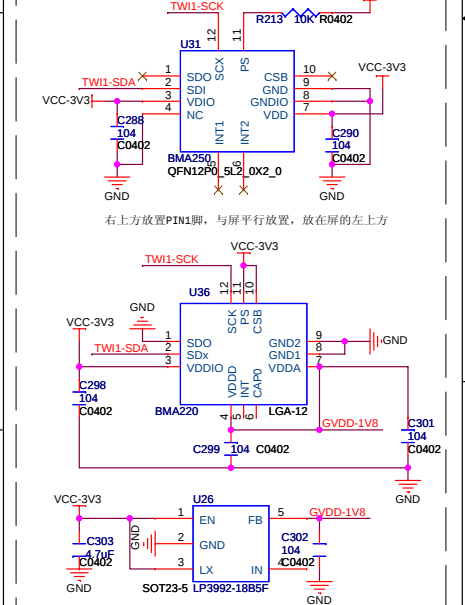
G-SENSOR



HEADPHONE & SPEAKER

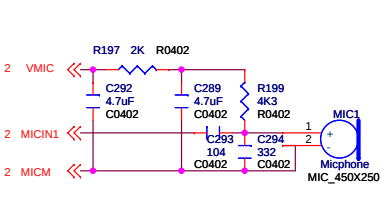


PCB上可以双layout

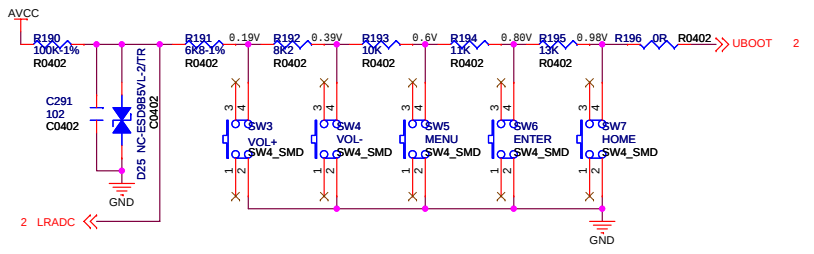


右上方放置PIN1脚, 与屏平行放置, 放在屏的左上方

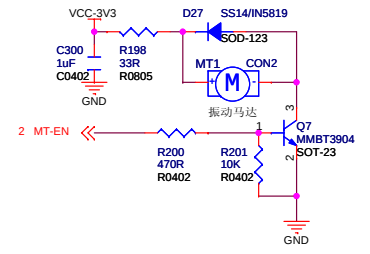
MIC



KEY

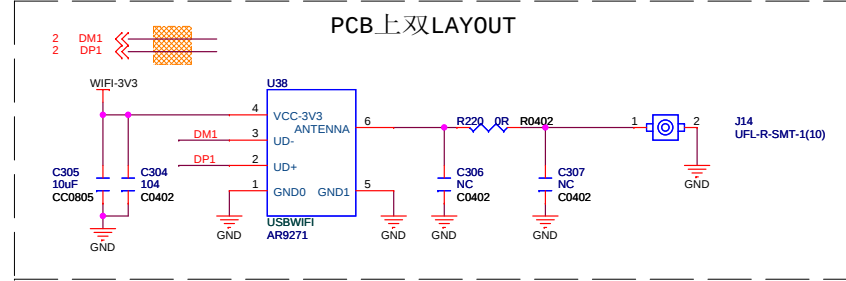
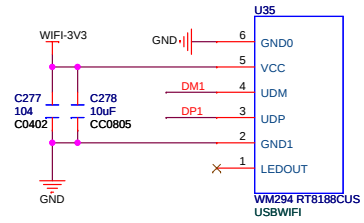


MOTOR



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WIFI



File		
APP3_PAD_DDR3		
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